EC–QDQUA–TE

DECchip 21064-150, -166, -200
Alpha AXP Microprocessor
Data Sheet Update

This document provides updated information for the *DECchip 21064-150, -166, -200 Alpha AXP Microprocessors Data Sheet*, EB-N0136-72.

The following table replaces Table 19, **21064 Maximum Ratings**.

Characteristics	Ratings
Storage temperature	–55°C to 125°C (–67°F to 257°F)
Supply voltage	Vss –0.5 V, Vdd 3.6 V
Junction temperature	90°C (194°F)
Voltage applied to pins	
3 V tolerant pins	–0.5 V to Vdd + 0.5 V
5 V tolerant pins	–0.5 V to 5.5 V
Maximum power @Vdd=3.46 V:	
21064–150	21.0 W
21064–166	22.5 W
21064–200	27.0 W

Note

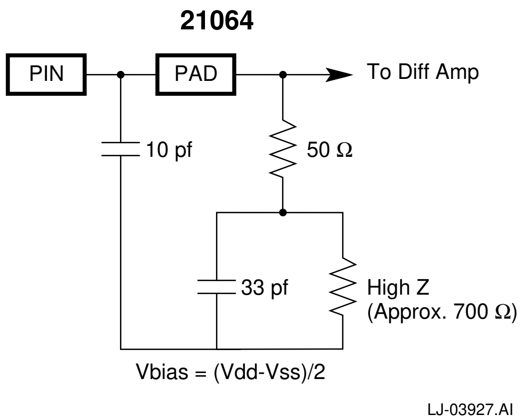
See the *DECchip 21064 and DECchip 21064A Alpha AXP Microprocessors Hardware Reference Manual* for formulas to calculate peak power and to calculate maximum power at other values of Vdd and clock frequency.

The following table replaces Table 20, **DC Input/Output Characteristics**.

Symbol	Description	Min	Max	Units	Test Conditions
Vdd	Power supply voltage	3.135	3.465	V	–
Vih	High-level input voltage (except dcOk_h and cont_l)	2.0	–	V	–
Vihs	High-level input voltage (static pins dcOk_h and cont_l)	2.7	–	V	–
Vil	Low-level input voltage	–	0.8	V	–
Voh	High-level output voltage Ioh = 100 μA	2.4	–	V	–
Vol	Low-level output voltage Iol = 3.2 mA	–	0.4	V	–
Vdiffc	Differential clock input swing (duty cycle 45–55%)	300 mV	3.0	V	–
Iil	Input leakage current (except eclOut_h)	–100	100	μA	0<Vin<Vdd V
Iel	Input leakage current (eclOut_h)	–150	150	μA	0<Vin<Vdd V
Ioz	Output leakage current (tristate)	–100	100	μA	–
Icin	Clock input leakage	–4	4	mA	0<Vin<3.465 V

Note: Values in this table are valid only for Vref = 1.4 V.

The following figure replaces Figure 2, **Clock Termination**.



The following figure replaces Figure 14, **Package Dimensions**.